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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Group Art Unit 2839

Patent Application of
Warren A. Seith, et al.
Application No. 10/681,733
Confirmation No.: 6695
Filed: October 8, 2003

Examiner: Javaid H. Nasri

"CIRCUIT BOARD ASSEMBLY, MAIN
AND CONNECTOR BOARDS, AND
CONNECTOR PINS FOR SAME"

I, Mary A. Azzolina, hereby certify that this correspondence is being deposited with the US Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date of my signature.

Mary A. Azzolina
Signature
2/18/2005
Date of Signature

SUBMISSION OF FORMAL DRAWINGS

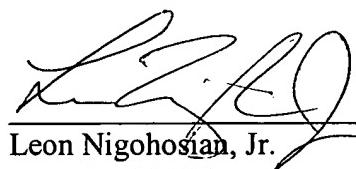
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Sir:

Enclosed for filing are the formal drawings for the above-identified application. Although not requested by the Office, entry of the enclosed drawings is respectfully requested. The enclosed drawings should be used for printing in the patent.

Respectfully submitted,

February 18, 2005
Date


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